

REMARKS

This Preliminary Amendment, being filed before receiving the first Office Action in the present application, substitutes a new set of claims 63 – 119 for the original remaining original application claims 56 – 62, which are being cancelled.

Claims 63 – 89 are directed to the use of a write cache memory in a non-volatile memory system. An earlier U.S. patent no. 5,936,971, issued from a continuation application having the same priority as the present application, also contains claims to the use of a cache memory but of different scope.

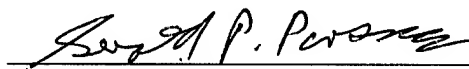
Claims 90 – 119 are similar to a group of claims 1 – 31 of U.S. patent no. 6,149,316, granted from a co-pending parent to the present application, but are of different scope, primarily by the elimination of unnecessary limitations.

An early examination and allowance of the claims being substituted by this Preliminary Amendment are respectfully requested. A comprehensive Information Disclosure Statement is being prepared, and it is planned to file it within a few weeks.

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Respectfully submitted,



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Sept. 19, 2001
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Version with markings to show changes made

Page 1 of the specification, between lines 3 and 4, rewrite the paragraph that was added by paragraph 4 of the Continuation Application Transmittal, filed with the present continuation application, as follows:

Cross-Reference to Related Applications

This is a continuation of Application Serial No. 08/789,421, filed January 29, 1997, now ; Patent No. 6,149,316; which in turn is a continuation of Serial No. 08/174,768, filed December 29, 1993, now Patent No. 5,602,987; which in turn is a continuation of Serial No. 07/963,838; filed October 20, 1992, now Patent No. 5,297,148; which in turn is a divisional of Serial No. 07/337,566; filed April 13, 1989, now abandoned;

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